



Docket No.: M4065.0451/P451-B
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
R. J. Baker

Application No.: 10/674,550

Confirmation No.: 5735

Filed: October 1, 2003

Art Unit: 2824

For: Resistive memory element sensing using
averaging

Examiner: A. Q. Tran

RESPONSE TO RESTRICTION REQUIREMENT

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the restriction requirement set forth in the Office Action mailed March 30, 2005 (Paper No. 03242005), applicant hereby elects Group I, claims 28-33, 38-39, and 52 for continued examination, without traverse.

The Examiner has required restriction between:

Group I. Claims 28-33, 38-39, and 52, drawn to a method of determining a value of a resistance and a device state sensing circuit;

Group II. Claims 34-37, and 40-46, drawn to a method of measuring a resistance; and

Group III. Claims 47-51, drawing to a method of making a memory integrated circuit.

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Prompt examination of the Group I claims 28-33, 38-39, and 52 is solicited.

Dated: April 26, 2005

Respectfully submitted,

By 

Thomas J. D'Amico

Registration No.: 28,371

DICKSTEIN SHAPIRO MORIN &

OSHINSKY LLP

2101 L Street NW

Washington, DC 20037-1526

(202) 785-9700

Attorney for Applicant